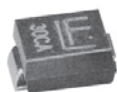


SILICON AVALANCHE DIODES 600 WATT SURFACE MOUNT TRANSIENT VOLTAGE SUPPRESSORS

ELECTRICAL SPECIFICATION @ TAMB 25°C

Cat. No.	Watts	Reverse Stand off Voltage V_R (Volts)	Breakdown Voltage V_{BR} (Volts) @ I_T			Maximum Clamping Voltage $V_C @ I_{FP}$ (Volts)	Maximum Peak Pulse Current I_{PP} (A)	Maximum Reverse Leakage $I_R @ V_R$ (μ A)	Net Price
			Min	Max	I_T (mA)				
UNI-POLAR									
SMBJ5.0A	600	5.0	6.40	7.00	9.2	65.3	800	.38	
SMBJ10A	600	10.0	11.10	12.30	17.0	35.3	5	.38	
SMBJ15A	600	15.0	16.70	18.50	24.4	24.6	5	.38	
SMBJ18A	600	18.0	20.00	22.10	29.2	20.6	5	.38	
SMBJ24A	600	24.0	26.70	29.50	38.9	15.5	5	.38	
SMBJ28A	600	28.0	31.10	34.40	45.4	13.3	5	.38	
SMBJ33A	600	33.0	36.70	40.60	53.3	11.3	5	.38	
SMBJ36A	600	36.0	40.00	44.20	58.1	10.4	5	.38	
P6SMBJ6.8A	600	5.80	6.45	7.14	10	10.5	1000	.38	
P6SMBJ10A	600	8.55	9.50	10.50	14.5	42.1	10	.38	
P6SMBJ15A	600	12.80	14.30	15.80	21.2	28.8	5	.38	
P6SMBJ18A	600	15.30	17.10	18.90	25.5	24.2	5	.38	
P6SMBJ24A	600	20.50	22.80	25.20	33.2	18.4	5	.38	
P6SMBJ27A	600	23.10	25.70	28.40	37.5	16.3	5	.38	
P6SMBJ33A	600	28.20	31.40	34.70	45.7	13.3	5	.38	
P6SMBJ36A	600	30.80	34.20	37.80	49.9	12.2	5	.38	
BI-POLAR									
SMBJ5.0CA	600	5.0	6.40	7.00	9.2	65.3	800	.42	
SMBJ10CA	600	10.0	11.10	12.30	17.0	35.3	5	.42	
SMBJ15CA	600	15.0	16.70	18.50	24.4	24.6	5	.42	
SMBJ18CA	600	18.0	20.00	22.10	29.2	20.6	5	.42	
SMBJ24CA	600	24.0	26.70	29.50	38.9	15.5	5	.42	
SMBJ28CA	600	28.0	31.10	34.40	45.4	13.3	5	.42	
SMBJ33CA	600	33.0	36.70	40.60	53.3	11.3	5	.42	
SMBJ36CA	600	36.0	40.00	44.20	58.1	10.4	5	.42	
P6SMBJ6.8CA	600	5.80	6.45	7.14	10	10.5	1000	.42	
P6SMBJ10CA	600	8.55	9.50	10.50	14.5	42.1	10	.42	
P6SMBJ15CA	600	12.80	14.30	15.80	21.2	28.8	5	.42	
P6SMBJ18CA	600	15.30	17.10	18.90	25.5	24.2	5	.42	
P6SMBJ24CA	600	20.50	22.80	25.20	33.2	18.4	5	.42	
P6SMBJ27CA	600	23.10	25.70	28.40	37.5	16.3	5	.42	
P6SMBJ33CA	600	28.20	31.40	34.70	45.7	13.3	5	.42	
P6SMBJ36CA	600	30.80	34.20	37.80	49.9	12.2	5	.42	

NOTE: For bi-directional type having V_{RWM} of 10 volts and less, the I_R limit is double. For parts without A, the V_{BR} is +10%



SILICON AVALANCHE DIODES 1000 WATT SURFACE MOUNT TRANSIENT VOLTAGE SUPPRESSORS

ELECTRICAL SPECIFICATION @ TAMB 25°C

Cat. No.	Watts	Reverse Stand off Voltage V_R (Volts)	Breakdown Voltage V_{BR} (Volts) @ I_T			Maximum Clamping Voltage $V_C @ I_{FP}$ (Volts)	Maximum Peak Pulse Current I_{PP} (A)	Maximum Reverse Leakage $I_R @ V_R$ (μ A)	Net Price
			Min	Max	I_T (mA)				
1KSMBJ6V8	1000	5.50	6.12	7.46	10.0	10.8	92.5	1000.0	.64
1KSMBJ10	1000	8.55	9.50	10.5	1.0	14.5	68.3	10.0	.64
1KSMBJ15	1000	12.8	14.3	15.8	1.0	21.2	46.7	5.0	.64
1KSMBJ18	1000	15.3	17.1	18.9	1.0	25.2	40.0	5.0	.64
1KSMBJ24	1000	20.5	22.8	25.2	1.0	33.2	30.0	5.0	.64
1KSMBJ27	1000	23.1	25.7	28.4	1.0	37.5	26.7	5.0	.64
1KSMBJ33	1000	28.2	31.4	34.7	1.0	45.7	22.0	5.0	.64
1KSMBJ36	1000	30.8	34.2	37.8	1.0	49.9	20.0	5.0	.64