

Very Low Forward Voltage Trench-based Schottky Rectifier

NRVTS2H60ESF, NRVTSM260EV2

Features

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- Low Thermal Resistance
- High Surge Capability
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These Devices are Pb–Free, Halogen Free/BFR Free and are RoHS Compliant

Mechanical Characteristics:

- Case: Molded Epoxy
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 11.7 mg (Approximately)
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Maximum for 10 Seconds
- MSL 1

Typical Applications

- Switching Power Supplies including Compact Adapters and Flat Panel Display
- High Frequency and DC-DC Converters
- Freewheeling and OR-ing diodes
- Reverse Battery Protection
- Instrumentation

TRENCH SCHOTTKY RECTIFIER 2.0 AMPERES 60 VOLTS

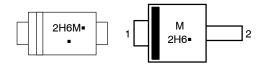




SOD-123FL CASE 498

POWERMITE CASE 457

MARKING DIAGRAMs



2H6 = Specific Device Code
M = Date Code
Device Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping [†]
NRVTS2H60ESFT1G	SOD-123FL (Pb-Free)	3,000 / Tape & Reel
NRVTS2H60ESFT3G	SOD-123FL (Pb-Free)	10,000 / Tape & Reel
NRVTSM260EV2T1G	Powermite (Pb-Free)	3,000 / Tape & Reel
NRVTSM260EV2T3G	Powermite (Pb-Free)	12,000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	60	V
Average Rectified Forward Current (T _L = 125°C)	lo	2.0	Α
Peak Repetitive Forward Current (Square Wave, 20 kHz, T _L = 139°C)	I _{FRM}	4.0	Α
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	50	Α
Storage and Operating Junction Temperature Range (Note 1)	T _{stg} , T _J	-65 to +175	°C

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
SOD-123FL	•		
Thermal Resistance, Junction-to-Lead (Note 2)	Ψ_{JCL}	24.4	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	85	°C/W
Thermal Resistance, Junction-to-Ambient (Note 3)	$R_{ hetaJA}$	330	°C/W
POWERMITE			
Thermal Resistance, Junction-to-Lead (Note 2)	Ψ_{JCL}	8.6	°C/W
Thermal Resistance, Junction-to-Ambient (Note 2)	$R_{ hetaJA}$	80	°C/W
Thermal Resistance, Junction-to-Ambient (Note 3)	$R_{ hetaJA}$	237	°C/W

ELECTRICAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Maximum Instantaneous Forward Voltage (Note 4) $ \begin{array}{l} (I_F=1.0 \text{ A}, T_J=25^{\circ}\text{C}) \\ (I_F=2.0 \text{ A}, T_J=25^{\circ}\text{C}) \\ (I_F=1.0 \text{ A}, T_J=125^{\circ}\text{C}) \\ (I_F=2.0 \text{ A}, T_J=125^{\circ}\text{C}) \end{array} $	V _F	0.55 0.65 0.47 0.58	V
Maximum Instantaneous Reverse Current (Note 4) (Rated dc Voltage, T _J = 25°C) (Rated dc Voltage, T _J = 125°C)	I _R	12 3	μA mA

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

- 2. Mounted with 700 mm² copper pad size (Approximately 1 in²) 1 oz FR4 Board.
- 3. Mounted with pad size approximately 20 mm² copper, 1 oz FR4 Board.
- 4. Pulse Test: Pulse Width \leq 380 μ s, Duty Cycle \leq 2.0%.

^{1.} The heat generated must be less than the thermal conductivity from Junction–to–Ambient: $dP_D/dT_J < 1/R_{\theta JA}$.

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TYPICAL CHARACTERISTICS

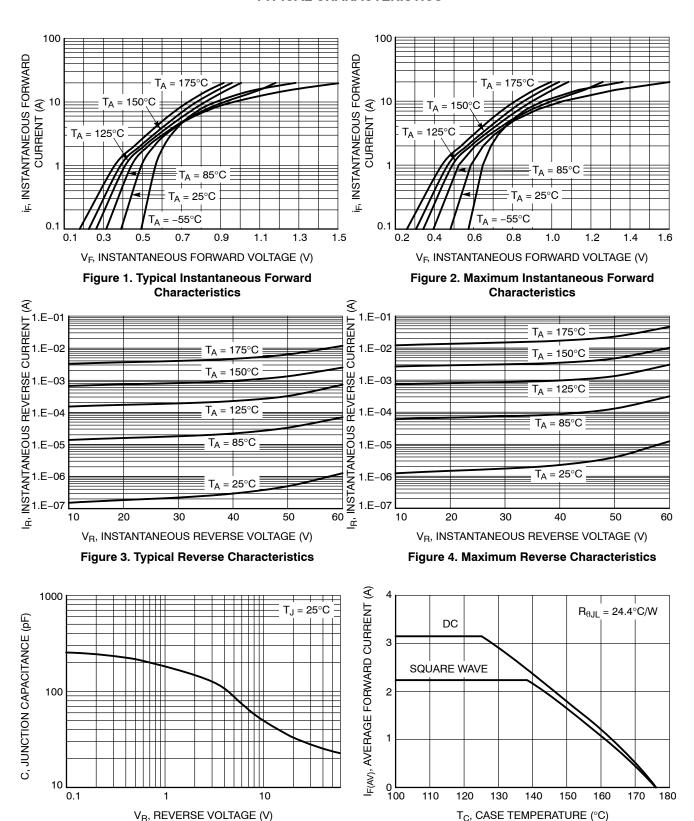
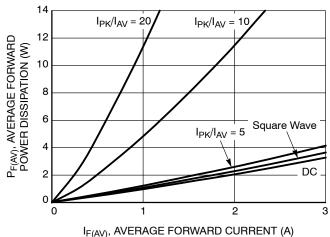


Figure 5. Typical Junction Capacitance

Figure 6. Current Derating

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TYPICAL CHARACTERISTICS



IF(AV), AVENAGE FORWARD CORNENT (A)

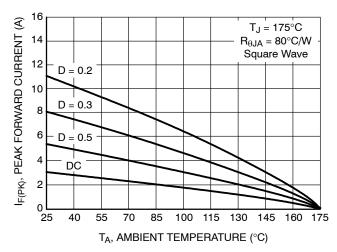
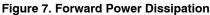


Figure 8. Forward Current Derating of Ambient Temperature



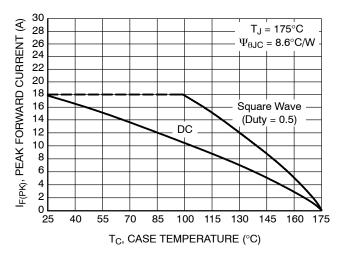


Figure 9. Forward Current Derating of Case Temperature

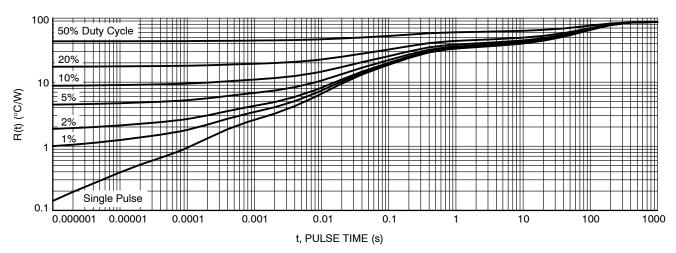


Figure 10. Thermal Characteristics

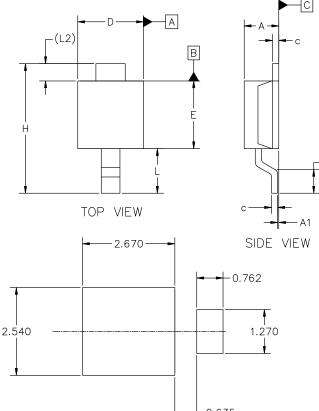




POWERMITE 1.90x1.96x1.00

CASE 457 ISSUE H

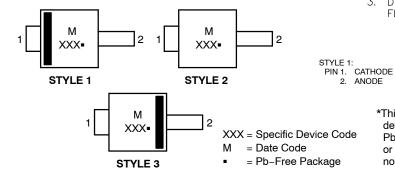
DATE 16 MAY 2025

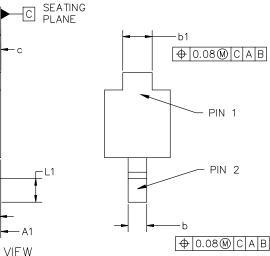


+0.635 RECOMMENDED MOUNTING FOOTPRINT

*For additional information on our Pb—Free Strategy and Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAMS*





BOTTOM VIEW

	MILLIM	ETERS	
DIM	MIN	NOM	MAX
А	0.85	1.00	1.15
A1	0.00	0.05	0.10
b	0.40	0.55	0.69
b1	0.70	0.85	1.00
С	0.10	0.18	0.25
D	1.75	1.90	2.05
Е	1.75	1.96	2.18
Н	3.60	3.75	3.90
L	1.20	1.35	1.50
L1	0.50	0.65	0.80
L2		0.50 REF	

NOTES:

- DIMENSIONING AND TOLERANCING AS PER
- ASME Y14.5M, 2018.
 CONTROLLING DIMENSION: MILLIMETERS.
 DIMENSION D & E DO NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

STYLE 2: PIN 1. ANODE OR CATHODE 2. CATHODE OR ANODE STYLE 3: PIN 1. ANODE 2. CATHODE

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "=", may or may not be present. Some products may not follow the Generic Marking.

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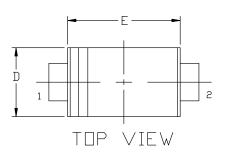
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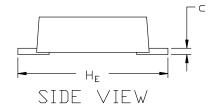


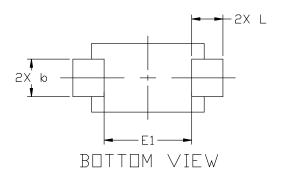


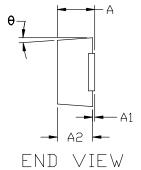
SOD-123-2 1.65x2.70x0.90 CASE 498 ISSUE E







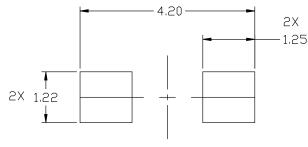




	MILLIMETERS		
DIM	MIN.	N□M.	MAX.
Α	0.90	0.95	0.98
A1	0.00	0.05	0.10
A2	0.85	0.90	0.95
b	0.70	0.90	1.10
U	0.10	0.15	0.20
D	1.50	1.65	1.80
E	2.50	2.70	2.90
E1	1.70	2.10	2.50
HE	3.40	3.60	3.80
L	0.55	0.75	0.95
θ	0°		8°

NOTES:

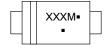
- 1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: MILLIMETERS
- DIMENSIONS 6 AND L ARE TO BE MEASURED ON A FLAT SECTION OF THE LEAD BETWEEN 0.10 AND 0.25 FROM THE LEAD TIP.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH PROTRUSIONS, OR GATE BURRS.
- 5. FLAT LEAD.



RECOMMENDED MOUNTING FOOTPRINT

For additional information on our Pb-Free strategy and soldering details, please download the IIN Semiconductor Soldering and Mounting Techniques Reference Manual, SILDERRM/D.

GENERIC MARKING DIAGRAM*



XXX = Specific Device Code

M = Date Code

= Pb-Free Package

(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "•", may or may not be present. Some products may not follow the Generic Marking.

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DESCRIPTION:	SOD-123-2 1.65x2.70x0.90)	PAGE 1 OF 1

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