

Interface module - UM 45-FLK16/ZFKDS - 2293530

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VARIOFACE module, with spring-cage connection and flat-ribbon cable connector, for mounting on NS 35/7.5, 16-pos.

The illustration shows a 20-position version



Key Commercial Data

Packing unit	1 STK
GTIN	 4 017918 120436

Technical data

Dimensions

Width	64 mm
Height	45 mm
Depth	50 mm

Ambient conditions

Ambient temperature (operation)	-20 °C ... 50 °C
Ambient temperature (storage/transport)	-20 °C ... 70 °C

General

Max. perm. operating voltage	< 50 V AC
	60 V DC
Max. perm. current (per branch)	1 A
Number of positions	16
Status display	No
Mounting position	any
Standards/regulations	IEC 60664
	DIN EN 50178
	IEC 62103

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Technical data

General

Degree of pollution	2
Overvoltage category	II

Connection data for connection 1

Connection name	Field level
Connection in acc. with standard	IEC / EN
Connection method	Spring-cage connection
Stripping length	8 mm
Conductor cross section solid	0.2 mm² ... 2.5 mm²
Conductor cross section flexible	0.2 mm² ... 1.5 mm²
Conductor cross section AWG	24 ... 14

Connection data for connection 2

Connection name	Controller level
Connection method	IDC/FLK pin strip (2.54 mm)
Number of connections	1
Number of positions	16

Standards and Regulations

Connection in acc. with standard	CUL
Standards/regulations	IEC 60664
	DIN EN 50178
	IEC 62103
Degree of pollution	2
Overvoltage category	II

Classifications

eCl@ss

eCl@ss 4.0	27250313
eCl@ss 4.1	27250313
eCl@ss 5.0	27250313
eCl@ss 5.1	27250313
eCl@ss 6.0	27242608
eCl@ss 7.0	27141152
eCl@ss 8.0	27141152

ETIM

ETIM 2.0	EC001434
ETIM 3.0	EC001604
ETIM 4.0	EC001604
ETIM 5.0	EC002780

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Classifications

UNSPSC

UNSPSC 6.01	30211824
UNSPSC 7.0901	39121421
UNSPSC 11	39121421
UNSPSC 12.01	39121421
UNSPSC 13.2	39121421

Approvals

Approvals

Approvals

UL Recognized / cUL Recognized / EAC / cULus Recognized

Ex Approvals

Approvals submitted

Approval details

UL Recognized 	
Nominal current I _N	1 A
Nominal voltage U _N	125 V

cUL Recognized 	
Nominal current I _N	1 A
Nominal voltage U _N	125 V

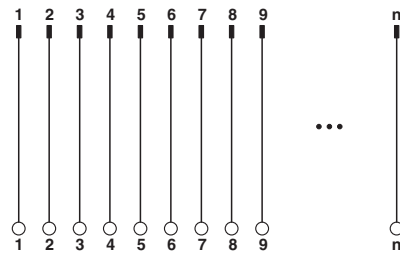
EAC

cULus Recognized 
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Drawings

Circuit diagram



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