

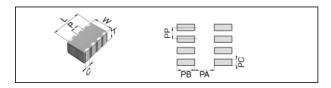
△TDK Multilayer Ceramic Chip Capacitors

Page 1 of 2

Creation Date: September 04, 2016

CKCL44C0G1H101K085AA(TDK item description: CKCL44C0G1H101KT****)

Applications Commercial Grade Feature 4-Array 4 in 1 Array CKCL44(2012) [EIA CC0805] Series











| Size | |
|------------------------------|------------------|
| Length(L) | 2.00mm +/-0.15mm |
| Width(W) | 1.25mm +/-0.15mm |
| Thickness(T) | 0.85mm +/-0.15mm |
| Terminal Width(C) | 0.25mm Min. |
| Terminal Spacing(P) | 0.50mm Min. |
| Recommended Land Pattern(PA) | 0.55mm Nom. |
| Recommended Land Pattern(PB) | 0.60mm Nom. |
| Recommended Land Pattern(PC) | 0.25mm Nom. |
| Recommended Land Pattern(PP) | 0.50mm Nom. |

| Electrical Characteristics | |
|----------------------------|---------------------|
| Capacitance | 100pF +/-10% |
| Rated Voltage | 50Vdc |
| Temperature Characteristic | C0G(0+/-30ppm/degC) |
| Q | 1000 Min. |
| Insulation Resistance | 10000MΩ Min. |

| Other | |
|------------------|------------------------------------|
| Soldering Method | Reflow |
| AEC Q200 | No |
| Packing | Punched (Paper)Taping [180mm Reel] |
| Package Quantity | 4000Pcs Min. |

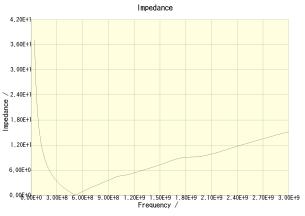
[•]This PDF document was created based on the data listed on the TDK Corporation website.

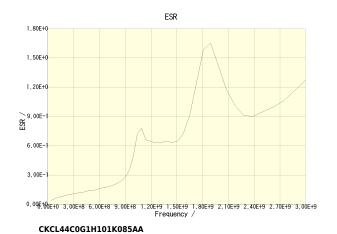
[•]All specifications are subject to change without notice.

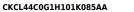
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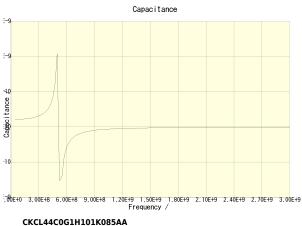
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Characteristic Graphs (This is reference data, and does not guarantee the product's characteristics.)



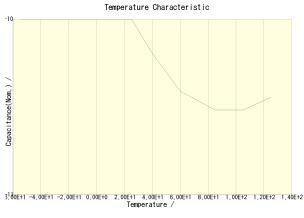




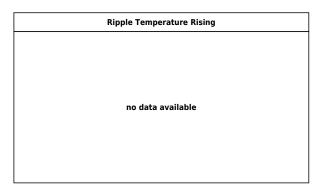








CKCL44C0G1H101K085AA



CKCL44C0G1H101K085AA(DC Bias = (CK)CL44C0G1H101K085AA(DC Bias = 25V)

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