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### LCD's Made Simple

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#### **TFT Display Module**

#### Part Number

E101RGD1280LBAM350-C

#### Overview

Display area: 229.46mm x 149.1mm (800 X 480)

Digital Interface, Black LED backlight, IPS, Wide temp, Transmissive, Capacitive Touch Screen, 350 NITS, RoHS Compliant

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# 1. General Specifications

No.	Item	Specification	Remark
1	LCD size	10.1 inch(Diagonal)	
2	Driver element	a-Si TFT active matrix	
3	Resolution	1280 × 3(RGB) × 800	
4	Display mode	Normally Black, Transmissive	
5	Dot pitch	0.0565(W) × 0.1695(H) mm	
6	Active area	216.96(W) × 135.60(H) mm	
7	Module size	229.46(W) ×149.1(H) ×2.50(D) mm	Note 1
8	Surface treatment	НС	
9	Color arrangement	RGB-stripe	
10	Interface	Digital	
11	Backlight power consumption	2.57 W	
12	Panel power consumption	1W	Note 2
13	Weight	TBD(Max)	

Note 1: Refer to Mechanical Drawing. Note 2: Including T-con Board power consumption

# 2. Pin Assignment

A 40pin connector is used for the module electronics interface. The recommended model is F62240-H1210A manufactured by Vigorconn.

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	Р	Common Voltage	
2	VDD	Р	Power Supply	
3	VDD	Р	Power Supply	
4	NC		No connection	
5	NC		No connection	
6	NC		No connection	
7	GND	Р	Ground	
8	Rxin0-	I	-LVDS Differential Data Input	R0-R5, G0
9	Rxin0+	I	+LVDS Differential Data Input	- K0-K3, G0
10	GND	Р	Ground	
11	Rxin1-	I	-LVDS Differential Data Input	C1 C5 D0 D1
12	Rxin1+	Rxin1+ I +LVDS Differential Data Inpu		G1~G5, B0,B1
13	GND	Р	Ground	
14	Rxin2-	I	-LVDS Differential Data Input	B2-B5,HS,VS,
15	Rxin2+	I	+LVDS Differential Data Input	DE
16	GND	Р	Ground	
17	RxCLK-	I	-LVDS Differential Clock Input	- LVDS CLK
18	RxCLK+	I	+LVDS Differential Clock Input	LVDS CLK
19	GND	Р	Ground	
20	Rxin3-	I	-LVDS Differential Data Input	R6, R7, G6, G7,
21	Rxin3+	I	+LVDS Differential Data Input	B6, B7
22	GND	Р	Ground	
23	NC		No connection	
24	NC		No connection	
25	GND	Р	Ground	
26	NC		No connection	

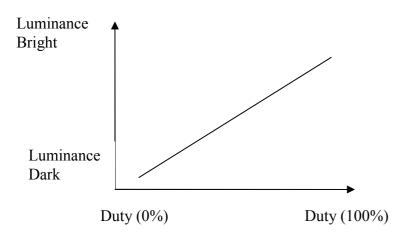
27	LED_PWM	Ο	CABC controller signal output for backlight	Note2
28	NC		No connection	
29	AVDD	Р	Power for Analog Circuit	
30	GND	Р	Ground	
31	LED-	Р	LED Cathode	
32	LED-	Р	LED Cathode	
33	NC		No connection	
34	NC		No connection	
35	VGL	Р	Gate OFF Voltage	
36	NC		No connection	
37	CABC_EN	I	CABC Enable Input	Note1
38	VGH	Р	Gate ON Voltage	
39	LED+	Р	LED Anode	
40	LED+	Р	LED Anode	

I: input, O: output, P: Power

Note1: The setting of CABC function are as follows.

Pin	Enable	Disable
CABC_EN	High Voltage	Low Voltage or open

Note2: LED\_PWM is used to adjust backlight brightness.



# 3. Operation Specifications

## 3.1. Absolute Maximum Ratings

(Note 1)

		Mal.			
Item	Symbol	Vai	ues	Unit	Remark
Kom	- Cymber	Min.	Max.	Onne	Roman
	VDD	-0.3	3.9	V	
	AVDD	-0.3	14	V	
Power voltage	$V_{GH}$	-0.3	42.0	V	
	$V_{GL}$	-19	0.3	V	
	$V_{GH}$ - $V_{GL}$	12	40.0	V	
Operation Temperature	T <sub>OP</sub>	0	50	°C	
Storage Temperature	T <sub>ST</sub>	-20	60	°C	
LED Reverse Voltage	VR			V	Each LED
LED Forward Current	lf			mA	Each LED

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

#### 3.1.1. Backlight Driving Conditions

Item	Symbol		Values	Unit	Remark		
item	Symbol	Min.	Тур.	Max.	Offic	Remark	
Voltage for LED backlight	$V_L$	8.7	9.3	9.9	V	Note 1	
Current for LED backlight	ΙL	-	260	-	mA		
LED life time	-	15000	20,000	-	Hr	Note 2	

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25 $^{\circ}$ C and I<sub>L</sub> =260mA. The LED lifetime could be decreased if operating I<sub>L</sub> is lager than 260mA.

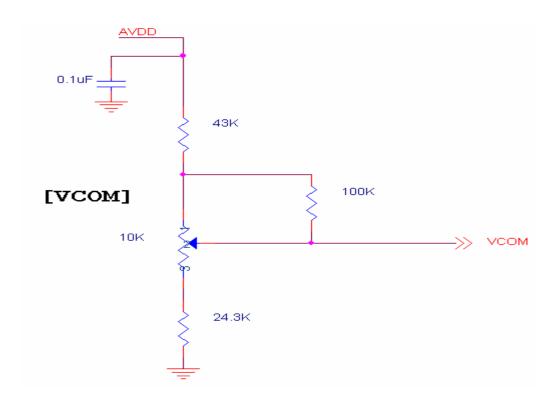
#### **Typical Operation Conditions** 3.2.

(Note 1)

Item	Symbol		Values	Unit	Remark	
item	Symbol	Min.	Тур.	Max.	Offic	Remark
	VDD	2.3	2.5	2.7	V	Note 2
Dower veltege	AVDD	8.0	8.2	8.4	V	
Power voltage	$V_{GH}$	21.7	22	22.3	V	
	$V_{GL}$	-7.3	-7	-6.7	V	
Input signal voltage	VCOM	3.0	3.3	3.6	V	Note 4
Input logic high voltage	V <sub>IH</sub>	0.8 VDD	-	VDD	V	Note 2
Input logic low voltage	V <sub>IL</sub>	0	-	0.2 DV <sub>DD</sub>	V	Note 3

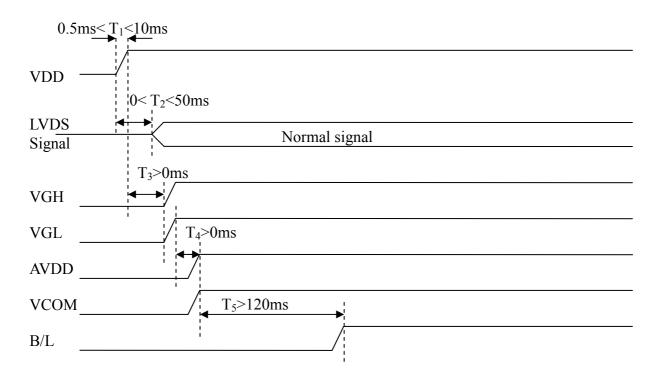
Note 1: Be sure to apply VDD and  $V_{GL}$  to the LCD first, and then apply  $V_{GH}$ . Note 2: VDD setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 4: Typical VCOM is only a reference value, it must be optimized according to each LCM. Be sure to use VR.

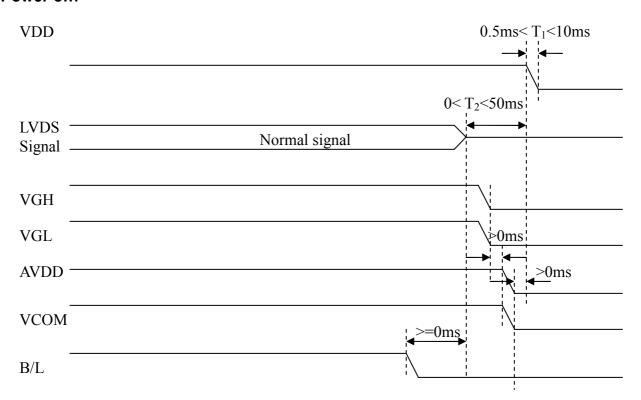


## 3.3. Power Sequence

#### a. Power on:



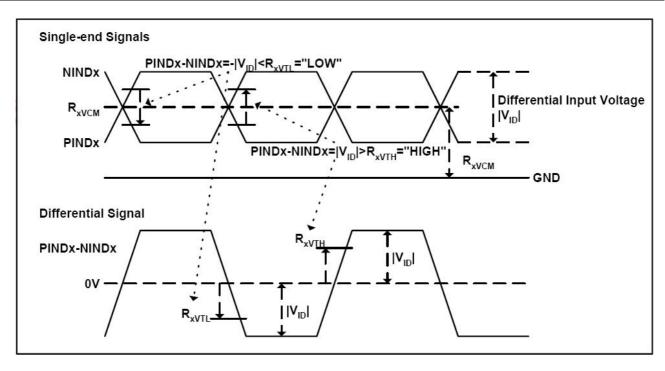
#### b. Power off:



# 3.4. LVDS Signal Timing Characteristics

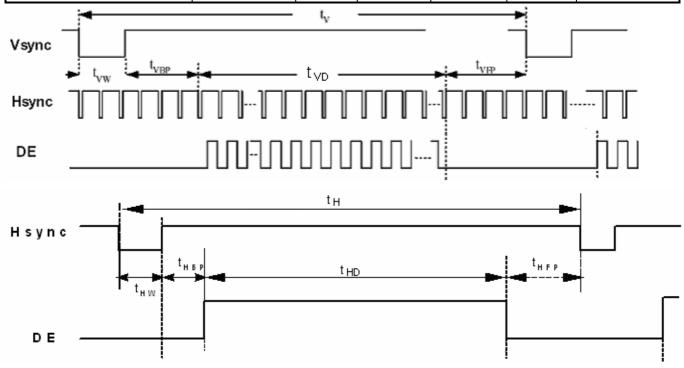
#### 3.4.1. AC Electrical Characteristics

Parameter	Symbol		Values	Unit	Remark	
		Min.	Тур.	Max.		
LVDS Differential input high Threshold voltage	R <sub>xVTH</sub>	-	-	+100	mV	R <sub>XVCM</sub> =1.2V
LVDS Differential input low Threshold voltage	R <sub>xVTL</sub>	-100	-	-	mV	TXXVCM— 1.2 V
LVDS Differential input common mode voltage	R <sub>xVCM</sub>	0.7	1	1.6	V	
LVDS Differential voltage	V <sub>ID</sub>	100	-	600	mV	

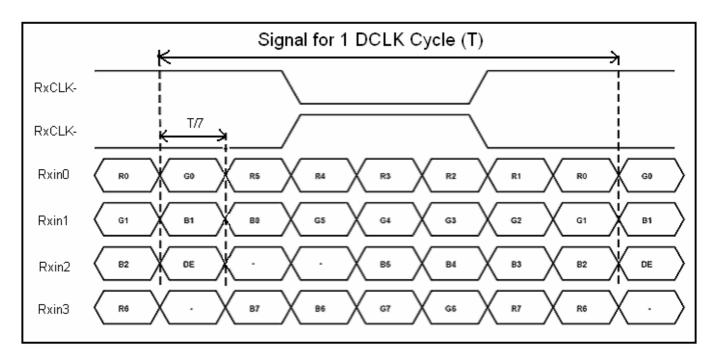


## 3.4.2. Timing Table

ltom	Symbol		Values		l loit	Domonic
Item	Symbol	Min.	Тур.	Max.	Unit	Remark
Clock Frequency	1/Tc	(68.9)	71.1	(73.4)	MHz	Frame rate =60Hz
Horizontal display area	tHD		1280		Тс	
HS period time	tн	(1410)	1440	(1470)	Тс	
HS Width +Back Porch +Front Porch	thw+ thbp +thfp	(60)	160	(190)	Тс	
Vertical display area	tvo		800		tн	
VS period time	tv	(815)	823	(833)	tн	
VS Width +Back Porch +Front Porch	tvw+ tv <sub>BP</sub> +tv <sub>FP</sub>	(15)	23	(33)	tн	



## 3.4.3. LVDS Data Input Format

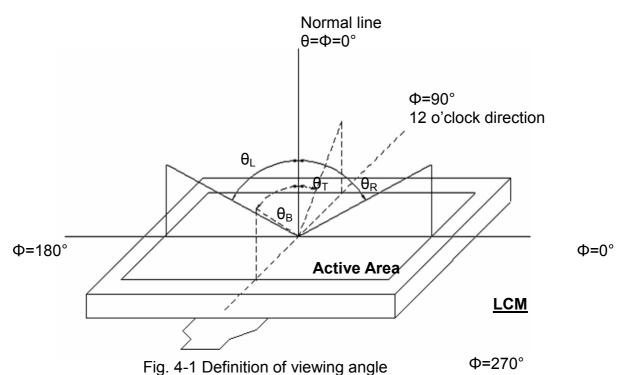


# 4. Optical Specifications

Item	Symbol	Condition	Values			Unit	Remark	
item	Symbol	Condition	Min.	Тур.	Max.	Oilit	Remark	
	$\theta_{L}$	Ф=180°(9 o'clock)	75	85	-			
Viewing angle	$\theta_{R}$	Ф=0°(3 o'clock)	75	85	-	dograd	Note 1	
(CR≥ 10)	θτ	Φ=90°(12 o'clock)	75	85	-	degree	Note 1	
	$\theta_{B}$	Ф=270°(6 o'clock)	75	85	-			
Doggoog time	T <sub>ON</sub>		-	10	20	msec	Note 3	
Response time	T <sub>OFF</sub>		-	15	30	msec	Note 3	
Contrast ratio	CR		600	800	-	-	Note 4	
	W <sub>X</sub>	Normal θ=Φ=0°	0.26	0.31	0.36	-	Note 2	
Color chromaticity	W <sub>Y</sub>		0.28	0.33	0.38	-	Note 5 Note 6	
Luminance	L		300	350	400	cd/m2	Note 6	
Luminance uniformity	Yu		70	75	-	%	Note 7	

#### **Test Conditions:**

- 2. The test systems refer to Note 2.



Note 2: Definition of optical measurement system.

6 o'clock

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Viewing angle is measured by ELDIM-EZ contrast/Height :1.2mm, Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/ Field of view: 1° /Height: 500mm.)

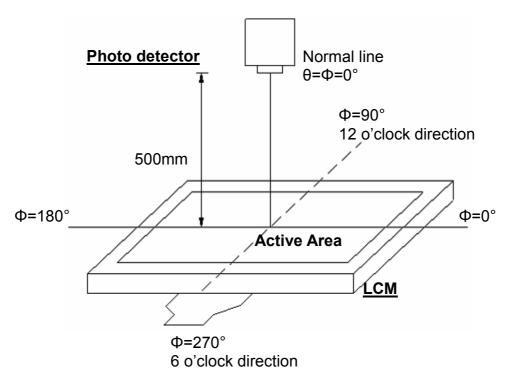


Fig. 4-2 Optical measurement system setup

#### Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time  $(T_{ON})$  is the time between photo detector output intensity changed from 90% to 10%. And fall time  $(T_{OFF})$  is the time between photo detector output intensity changed from 10% to 90%.

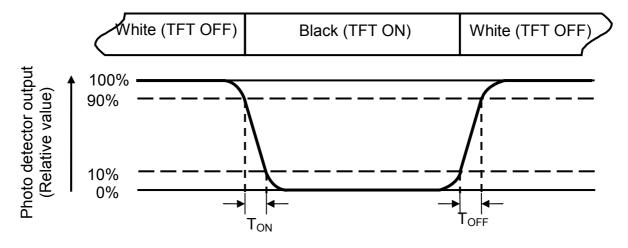


Fig. 4-3 Definition of response time

#### Note 4: Definition of contrast ratio

Contrast ratio (CR) =  $\frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$ 

Note 5: Definition of color chromaticity (CIE1931)

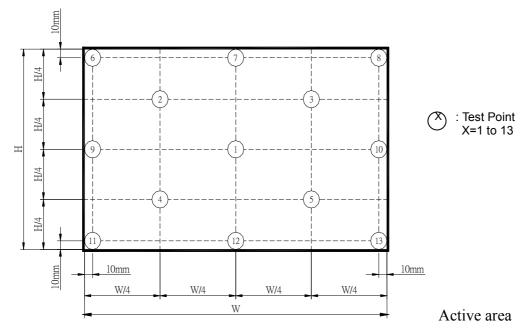
Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is  $I_1$  = 260mA.

#### Note 7: Definition of Luminance Uniformity

Measure the luminance of gray level 63 at 9 points

 $\delta W_{9p} = \{ \text{Minimum [L (1)+ L (6)+ L (7)+ L (8)+ L (9)+ L (10)+ L (11) +L (12) +L (13)] } / \\ \text{Maximum [L (1)+ L (6)+ L (7)+ L (8)+ L (9)+ L (10)+ L (11) +L (12) +L (13)]} \} *100\%$ 



# 5. Reliability Test Items

(Note3)

Item	Test	Conditions	Rem	ark
High Temperature Storage	Ta = 60°C	120hrs	Note 1,	Note 4
Low Temperature Storage	Ta = -20°C	120hrs	Note 1,	Note 4
High Temperature Operation	Ts = 50°C	120hrs	Note 2,	Note 4
Low Temperature Operation	Ta = 0°C	120hrs	Note 1,	Note 4
Operate at High Temperature and Humidity	+40°C, 90%RH	120hrs	Note 4	
Thermal Shock	_	$0^{\circ}\mathbb{C}/30$ min for a total 100 old temperature and end ure.	Note 4	
Vibration Test	Frequency range:1 Stroke:1.5mm Sweep:10Hz~55Hz 2 hours for each di (6 hours for total)	z~10Hz		
Mechanical Shock	100G 6ms,±X, ±Y, direction	±Z 3 times for each		
Package Vibration Test	Random Vibration ISTA-3A 1Hz~200l Half hours for direct	Hz,Grms=0.53		
Package Drop Test	Height:60 cm 1 corner, 3 edges,	6 surfaces		
Electro Static Discharge	± 2KV, Human Bo	ody Mode, 100pF/1500Ω		

- Note 1: Ta is the ambient temperature of samples.
- Note 2: Ts is the temperature of panel's surface.
- Note 3: In the standard condition, there shall be no practical problem that may affect the display function. After the reliability test, the product only guarantees operation, but don't guarantee all of the cosmetic specification.
- Note 4: Before cosmetic and function test, the product must have enough recovery time, at least 2 hours at room temperature.

#### 6. General Precautions

## 6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

### 6.2. Handling

- 1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
- 2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
- 3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
  - 4. Keep a space so that the LCD panels do not touch other components.
- 5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
- 6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
  - 7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

### 6.3. Static Electricity

- 1. Be sure to ground module before turning on power or operating module.
- 2. Do not apply voltage which exceeds the absolute maximum rating value.

## 6.4. Storage

- 1. Store the module in a dark room where must keep at 25±10° and 65%RH or less.
- 2. Do not store the module in surroundings containing organic solvent or corrosive gas.
  - 3. Store the module in an anti-electrostatic container or bag.

## 6.5. Cleaning

- 1. Do not wipe the polarizer with dry cloth. It might cause scratch.
- 2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

# 7. Mechanical Drawing

