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RGB Character Display Module

Part Number

E104RG88060LB6M400-C

Overview

Display area: 248.40mm x 195.40mm (800 X 600)
3.3V, 16/18/24-bit RGB Interface, White LED
backlight, Bottom view, Wide temp,
Transmissive-Normally White, Capacitive Touch
Screen, CTP Driver: GT9271, 400 NITS, RoHS
Compliant

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General Description

* Description

This is a color active matrix TFT (Thin Film Transistor) LCD (liquid crystal display) that uses amorphous silicon TFT as a switching device. This model is composed of a Transmissive type TFT-LCD Panel, driver circuit, back-light unit. The resolution of a 10.4'TFT-LCD contains 800x600 pixels, and can display up to 65K/262K/ 16.7M colors.

* Features

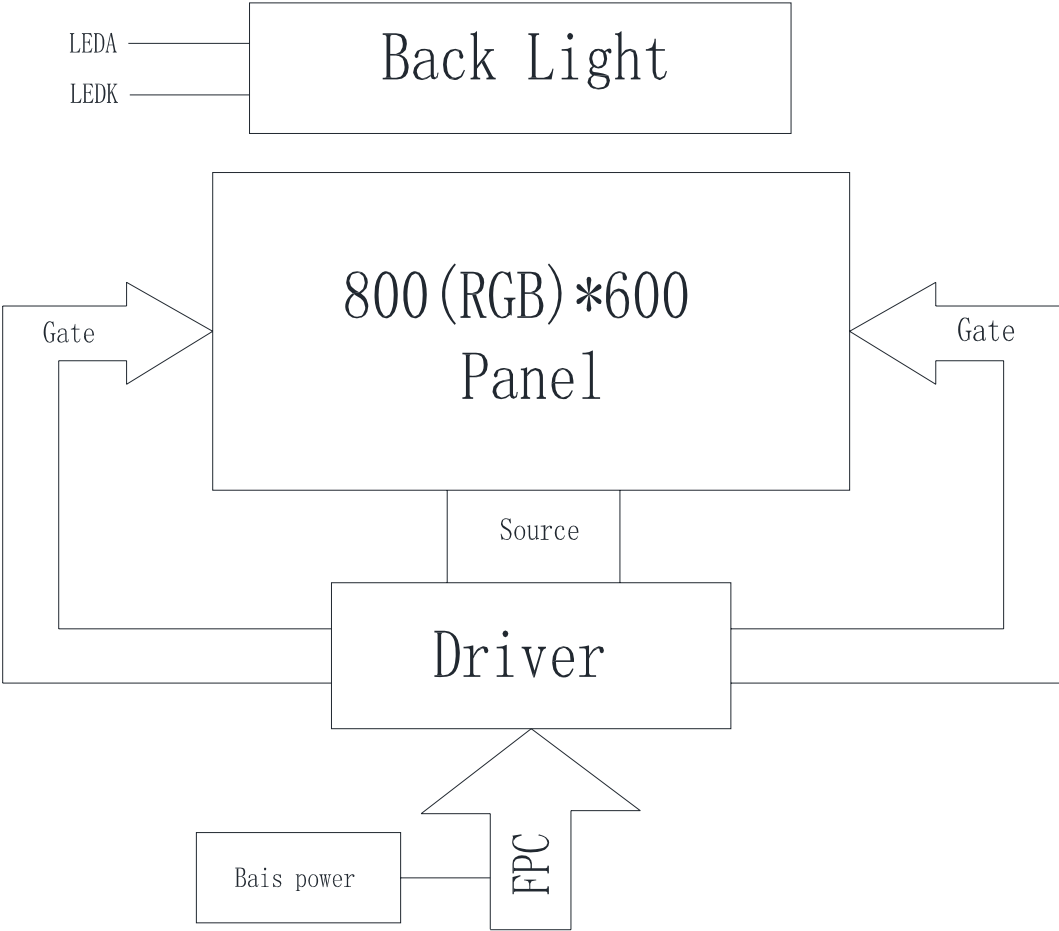
- Low Input Voltage: 3.3V (TYP)
- Display Colors of TFT LCD: 65K/262K/16.7M colors
- TFT Interface: 16/18/24 bit RGB
- CTP Interface: I2C

General Information Items	Specification	Unit	Note
	Main Panel		
TFT Display area(AA)	211.20(H)*158.40(V) (10.4inch)	mm	-
CTP View area	212.20(H)*159.40(V)	mm	
Driver element	TFT active matrix	-	-
Display colors	65K/262K/16.7M	colors	-
Number of pixels	800(RGB)*600	dots	-
TFT Pixel arrangement	RGB vertical stripe	-	-
Pixel pitch	0.264 (H) x0.264 (V)	mm	-
Viewing angle	6:00	o'clock	-
CTP Driver IC	GT9271		
Simultaneous Touch Points	10		
Display mode	Transmissive/ Normally White	-	-
Operating temperature	-20~+70	°C	-
Storage temperature	-30~+80	°C	-

* Mechanical Information

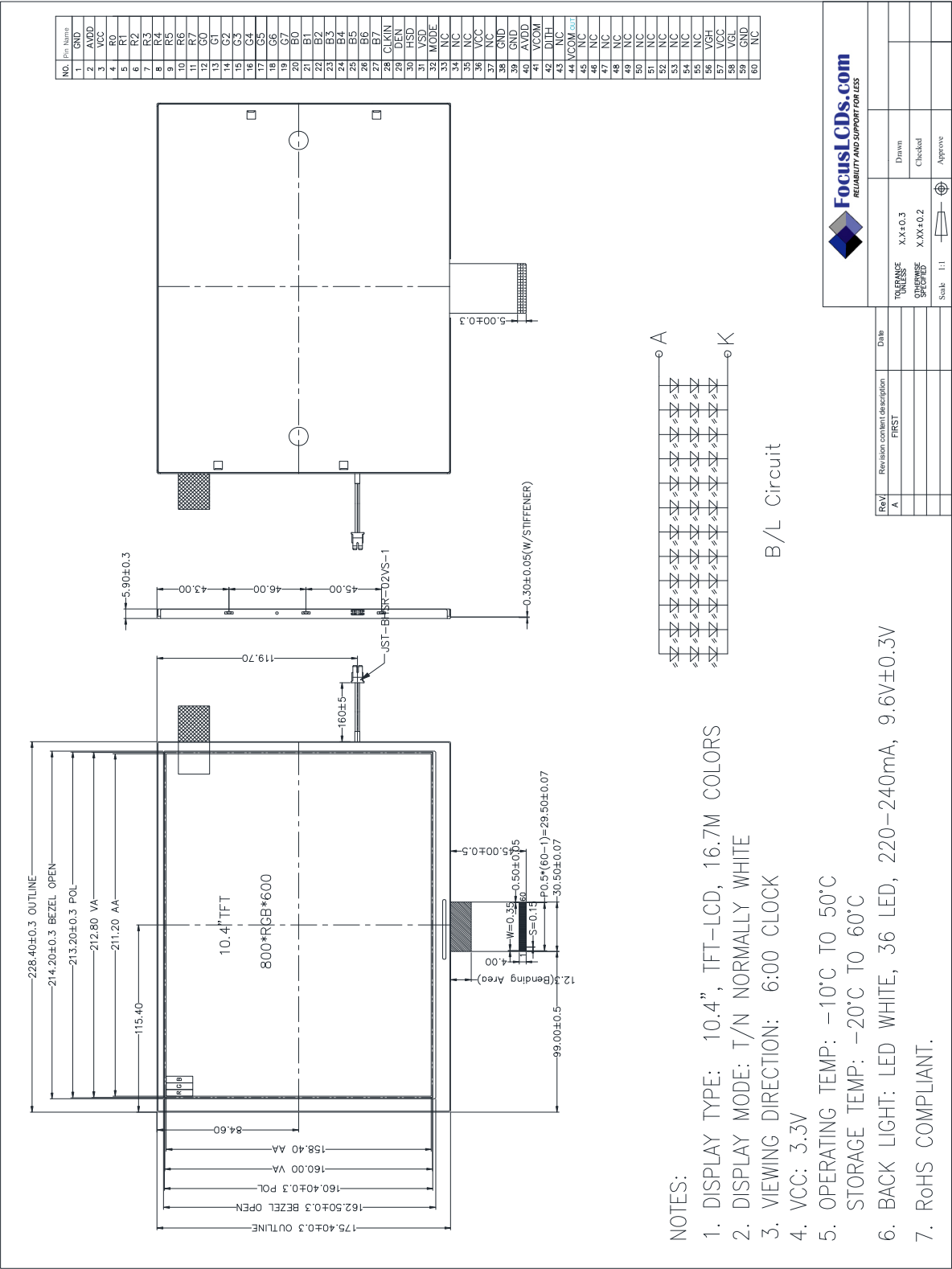
Item		Min.	Typ.	Max.	Unit	Note
Module size	Horizontal(H)		248.40		mm	-
	Vertical(V)		195.40		mm	-
	Depth(D)		8.23		mm	-
Weight			TBD		g	-

1. Block Diagram

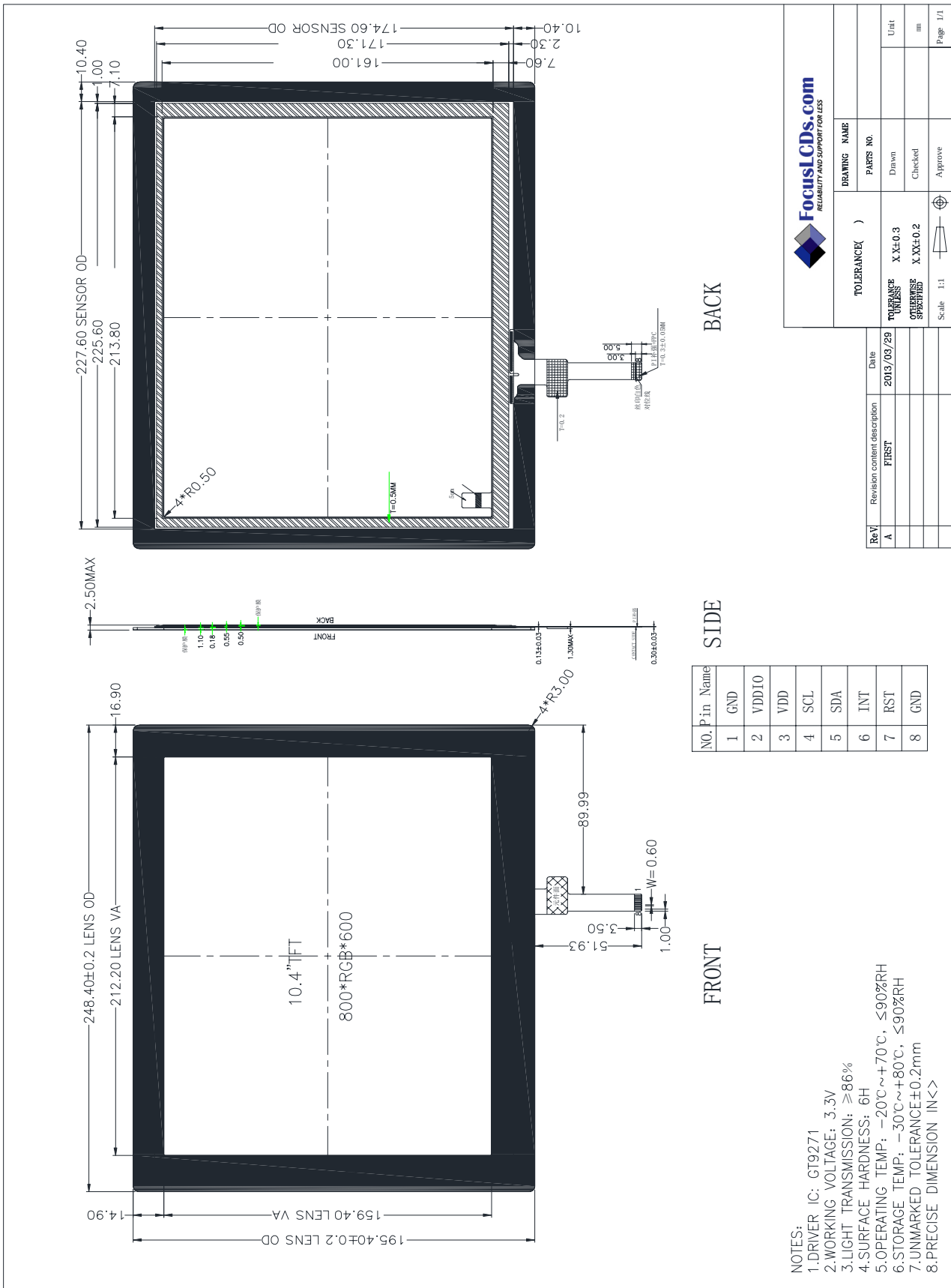


2. Outline dimension

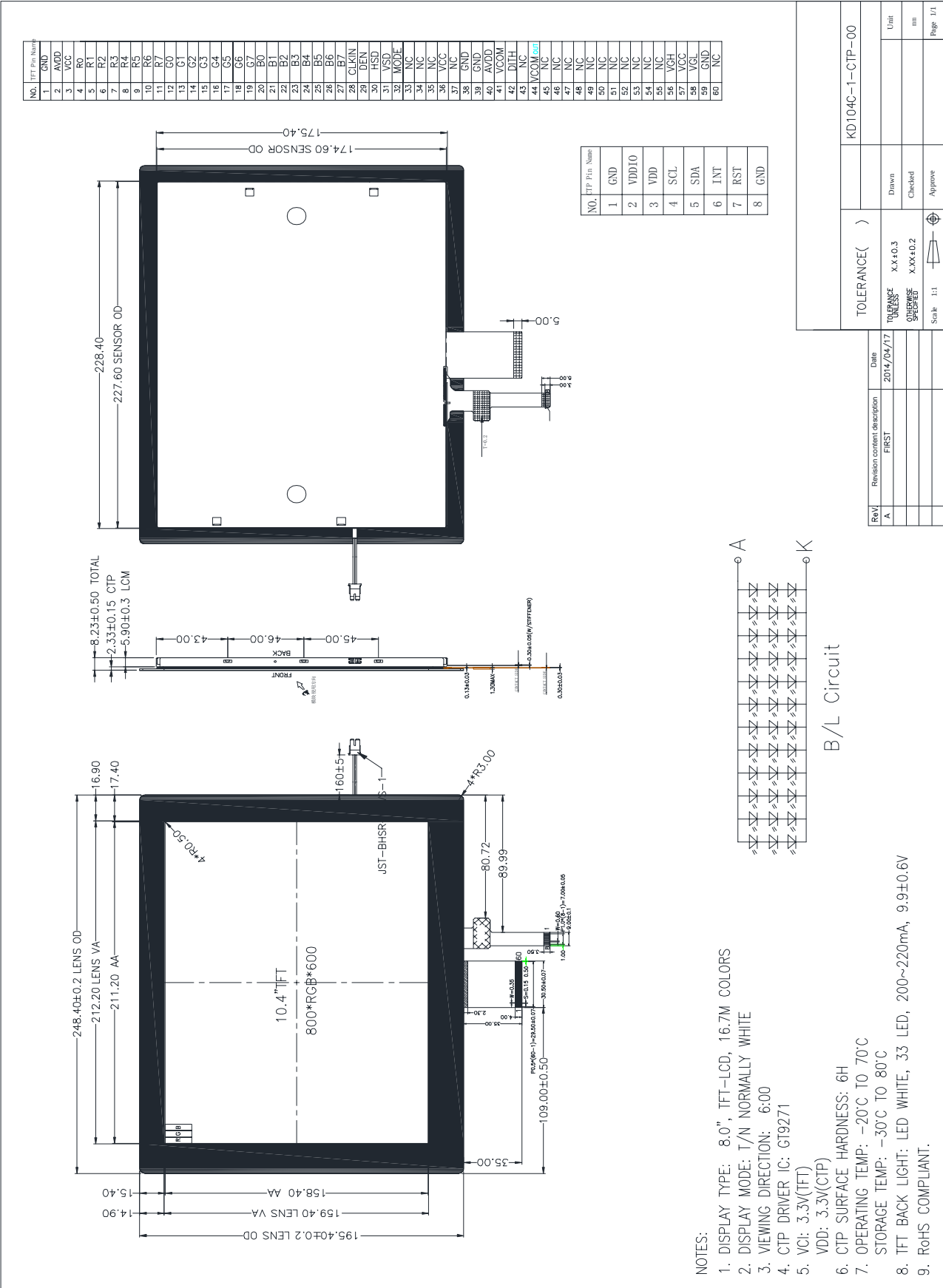
2.1 LCM



2.2 CTP



2.3 LCM+CTP



3. Input terminal Pin Assignment

3.1 TFT

NO.	SYMBOL	DISCRIPTION	I/O	REMARK
1	GND	Power Ground.	P	
2	AVDD	Analog input voltage	P	
3	VCC	Digital input voltage	P	
4	R0	Red data input(LSB)	I/O	
5	R1	Red data input	I/O	
6	R2	Red data input	I/O	
7	R3	Red data input	I/O	Note3
8	R4	Red data input	I/O	
9	R5	Red data input	I/O	
10	R6	Red data input	I/O	
11	R7	Red data input(MSB)	I/O	
12	G0	Green data input(LSB)	I/O	
13	G1	Green data input.	I/O	
14	G2	Green data input.	I/O	
15	G3	Green data input.	I/O	
16	G4	Green data input.	I/O	
17	G5	Green data input.	I/O	
18	G6	Green data input.	I/O	
19	G7	Green data input(MSB)	I/O	
20	B0	Blue data input(LSB)	I/O	
21	B1	Blue data input.	I/O	
22	B2	Blue data input.	I/O	
23	B3	Blue data input.	I/O	
24	B4	Blue data input.	I/O	
25	B5	Blue data input.	I/O	

26	B6	Blue data input.	I/O	
27	B7	Blue data input(MSB)	I/O	
28	CLKIN	Dot clock input.	I	
29	DEN	Data enable signal.	I	
30	HSD	HSYNC signal.	I	
31	VSD	VSYNC signal.	I	
32	MODE	H: DE mode (Default). L: SYNC mode.	I	
33	NC	No connect (please leave it open)	-	
34	NC	No connect (please leave it open)	-	
35	NC	No connect (please leave it open)	-	
36	VCC	Digital input voltage	P	
37	NC	No connect (please leave it open)	-	
38	GND	Power Ground.	P	
39	GND	Power Ground.	P	Note2,5
40	AVDD	Analog input voltage	P	Note2,5
41	VCOM	VCOM DC input	P	
42	DITH	Dithering function setting. H: Disable dithering function. L: Enable dithering function.	I	
43	NC	No connect (please leave it open)	-	
44	VCOM out	connect a capacitor.	O	Note1
45	NC	No connect (please leave it open)	-	
46	NC	No connect (please leave it open)	-	
47	NC	No connect (please leave it open)	-	Note 4
48	NC	No connect (please leave it open)	-	
49	NC	No connect (please leave it open)	-	
50	NC	No connect (please leave it open)	-	
51	NC	No connect (please leave it open)	-	

52	NC	No connect (please leave it open)	-	
53	NC	No connect (please leave it open)	-	
54	NC	No connect (please leave it open)	-	
55	NC	No connect (please leave it open)	-	
56	VGH	TFT turn on voltage.	P	
57	VCC	Digital input voltage	P	
58	VGL	TFT turn off voltage.	P	
59	GND	Power Ground.	P	
60	NC	No connect (please leave it open)	-	

Note:

(1) Suggested connector: MT-FP160N-2FR (UJU) or any equivalent.

3.2 CTP

NO.	SYMBOL	DISCRIPTION	I/O
1	GND	Ground.	P
2	VDDIO	I/O power supply voltage.	P
3	VDD	Supply voltage.	P
4	SCL	I2C clock input.	I
5	SDA	I2C data input and output	I/O
6	INT	External interrupt to the host.	I
7	RST	External Reset, Low is active.	I
8	GND	Ground.	P

4. LCD Optical Characteristics

4.1 Optical specification

(Ta = 25 ± 2℃, If=20mA)

Item		Symbol	Conditions	Specifications			Unit	Note
				Min.	Typ.	Max.		
Contrast Ratio		CR	Viewing normal angle θ= 0° Φ= 0° B/L On	300	500	--	--	(2),(4),(6)
Brightness		B		200	250	--	cd/m ²	
Response time		T _r		--	5	10	ms	(3)
		T _f		--	15	20	ms	
Chromaticity	Red	R _x		0.523	0.573	0.623	--	(2)(5)(6)
		R _y		0.30	0.350	0.40	--	
	Green	G _x		0.286	0.336	0.386	--	
		G _y		0.547	0.597	0.647	--	
	Blue	B _x		0.102	0.152	0.202	--	
		B _y		0.062	0.112	0.162	--	
	White	W _x		0.26	0.31	0.36	--	
		W _y		0.28	0.33	0.38	--	
Viewing Angle	Hor.	θ _L	CR ≥ 10 B/L On	60	70	-	deg.	(1)
		θ _R		60	70	-		
	Ver.	Φ _T		40	50	-		
		Φ _B		50	60	-		
NTSC		--	--	--	50	--	%	
Uniformity		--	--	--	80	--	%	(7)

Note:
(1) Definition of viewing angle range

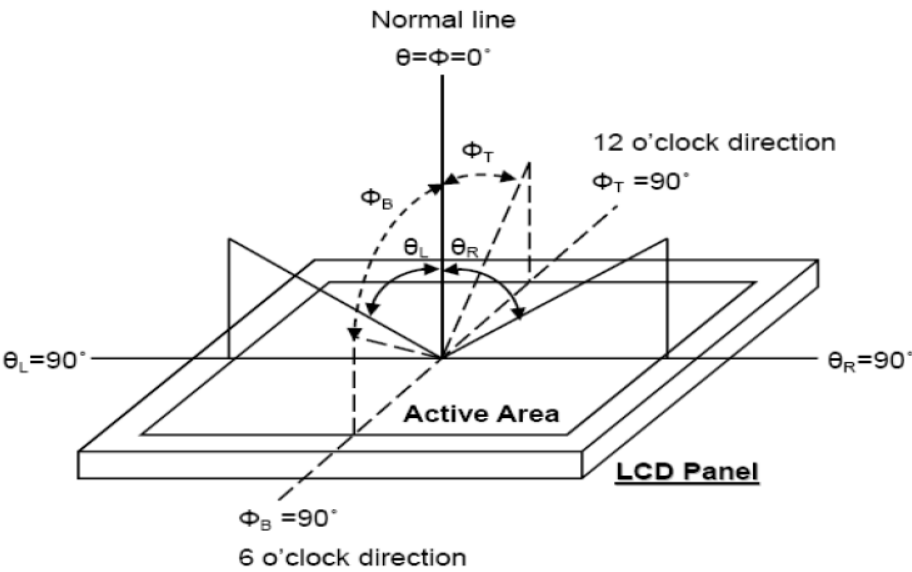
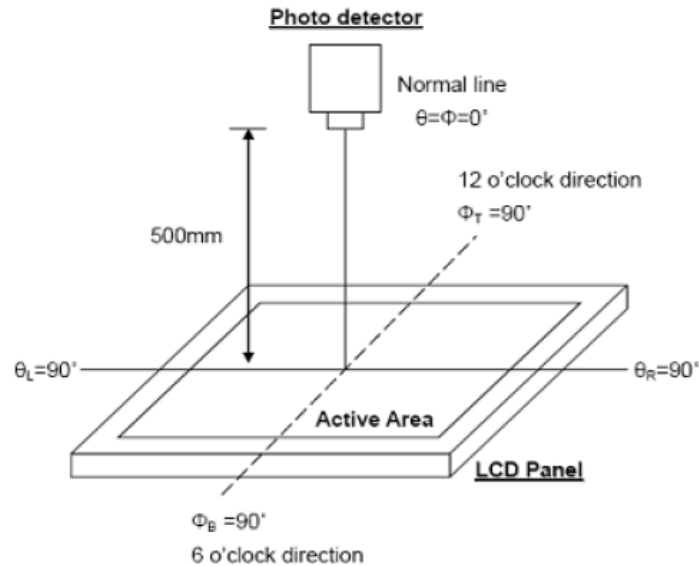


Fig. 8-1 Definition of viewing angle

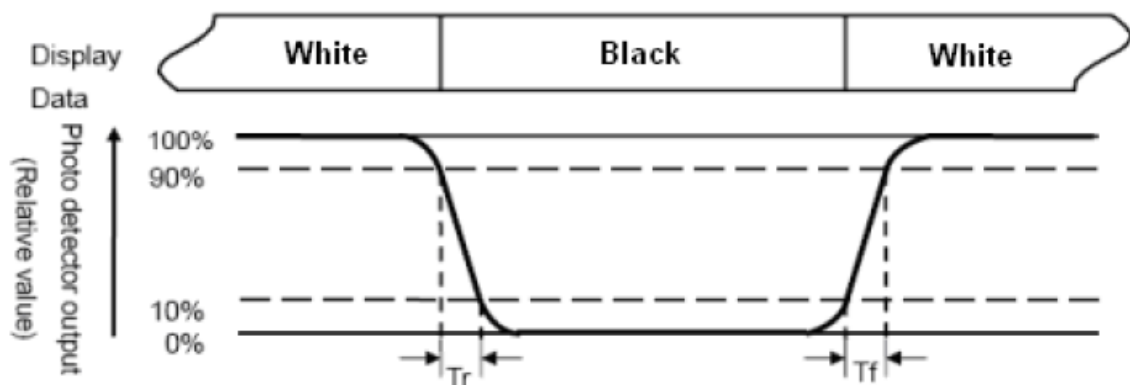
(2) Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7 luminance meter 1.0° field of view at a distance of 50cm and normal direction.



(3) Definition of Response time:

The response time is defined as the LCD optical switching time interval between “White” state And “Black” state. Rise time, T_r , is the time between photo detector output intensity changed from 90% to 10%. And fall time, T_f , is the time between photo detector output intensity changed from 10% to 90%.



(4) Definition of contrast ratio:

The contrast ratio is defined as the following expression.

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

(5) Definition of color chromaticity (CIE 1931)

Color coordinates measured at the center point of LCD

(6) Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

$$(7) \text{ Uniformity (U)} = \frac{\text{Brightness (min)}}{\text{Brightness (max)}} \times 100\%$$

5. TFT LCM Electrical Characteristics

5.1 Absolute Maximum Rating (Ta=25 VSS=0V)

Characteristics	Symbol	Min.	Max.	Unit
Digital Supply Voltage	VDD	-0.3	5.0	V
Digital interface supply Voltage	VDDIO	-0.3	VDD+0.3	V
Analog Power Supply Voltage	AV _{DD}	-0.5	15	V
TFT Device on voltage	V _{GH}	-0.3	42	V
TFT Device off voltage	V _{GL}	V _{GH} - 42	0.3	V
Operating temperature	T _{OP}	-10	+50	°C
Storage temperature	T _{ST}	-20	+60	°C

Note:

- (1) All of the voltages listed above are with respect to GND= 0V
- (2) Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above.

5.2 DC Electrical Characteristics

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Digital Supply Voltage	V _{DD}	3.0	3.3	3.6	V	
Digital interface supply Voltage	V _{DDIO}	3.0	3.3	3.6	V	
Analog Power Supply Voltage	AV _{DD}	10.3	10.5	10.7	V	
TFT Device on voltage	V _{GH}	-	18	-	V	
TFT Device off voltage	V _{GL}	-	-7	-	V	
Common Electrode Driving Voltage	V _{COM}	-	4.45	-	V	
Normal mode Current consumption	I _{CC}	24	30	45	mA	Black Pattern
	I _{AV_{DD}}	32	40	60		Black Pattern
Level input voltage	V _{IH}	0.7V _{DDIO}		V _{DDIO}	V	
	V _{IL}	GND		0.3 V _{DDIO}	V	
Level output voltage	V _{OH}	0.8V _{DDIO}		V _{DDIO}	V	
	V _{OL}	GND		0.2 V _{DDIO}	V	

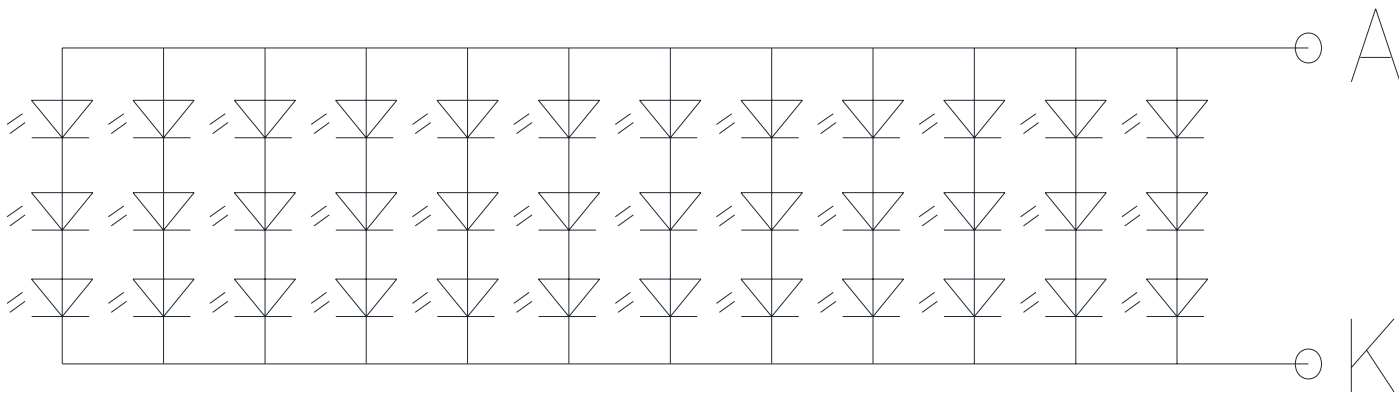
5.3 LED Backlight Characteristics

The back-light system is edge-lighting type with 27chips White LED

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Forward Current	I_F	162	180	198	mA	
Forward Voltage	V_F	9.3	9.9	10.5	V	Note1
LCM Luminance	L_V	400	-	-	cd/m2	
LED Life time	-	20000	-	-	Hr	Note2
Uniformity	AVg	80	--	--	%	

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and IL =180mA.

Note 2: The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25°C and IL =180mA. The LED lifetime could be decreased if operating IL is larger than 180 mA.



B/L Circuit

6. AC CHARACTERISTICS

6.1 Data Timing (Under frame rate is equal to 60Hz)

Horizontal Timing

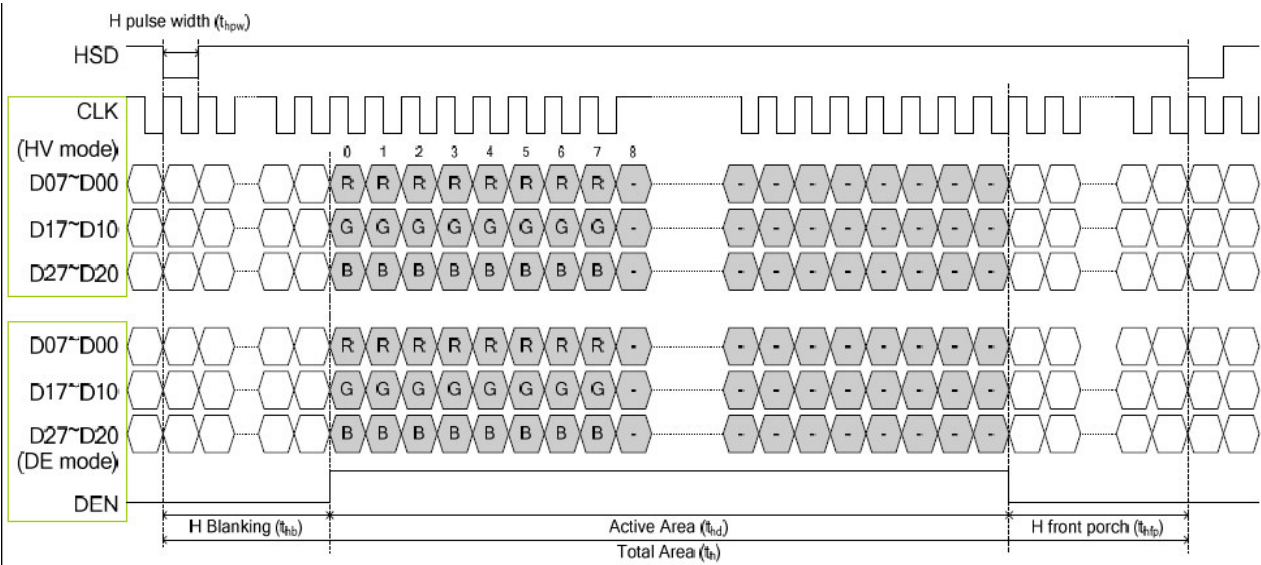
Parameter	Symbol	Spec			Unit
		Min.	Typ.	Max.	
Horizontal Display Area	thd	800			CLK
CLK Frequency	fclk	-	40	50	MHz
One Horizontal Line	th	862	1056	1200	CLK
HS Pulse Width	thpw	1	-	40	CLK
HS Back Porch	thb	46			CLK
HS Front Porch	thfp	16	210	354	CLK

Vertical Timing

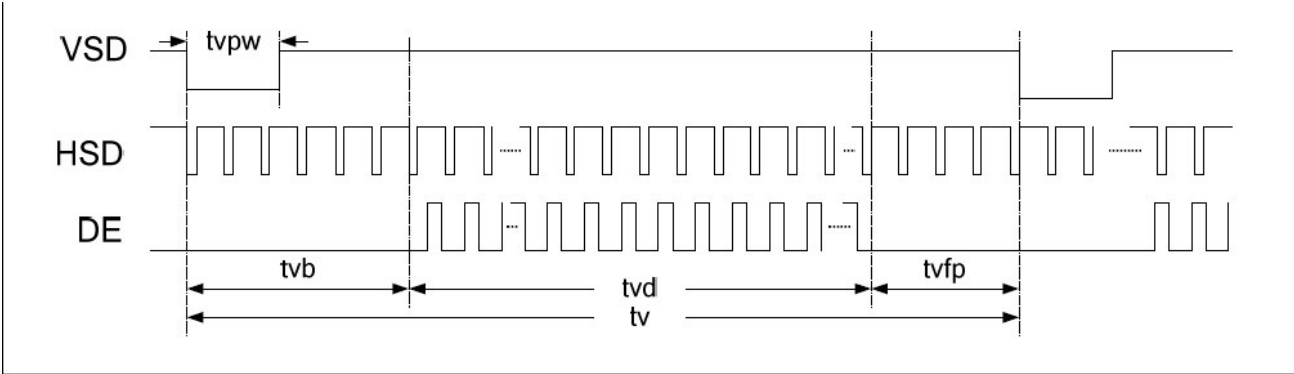
Parameter	Symbol	Spec			Unit
		Min.	Typ.	Max.	
Vertical Display Area	tvd	600			th
VS Period Time	tv	624	635	700	th
VS Pulse Width	tvpw	1	-	20	th
VS Back Porch	tvb	23	23	23	th
VS Front Porch	tvfp	1	12	77	th

Note:
CMO suggest using frame rate 60Hz to have better performance

Horizontal Timing



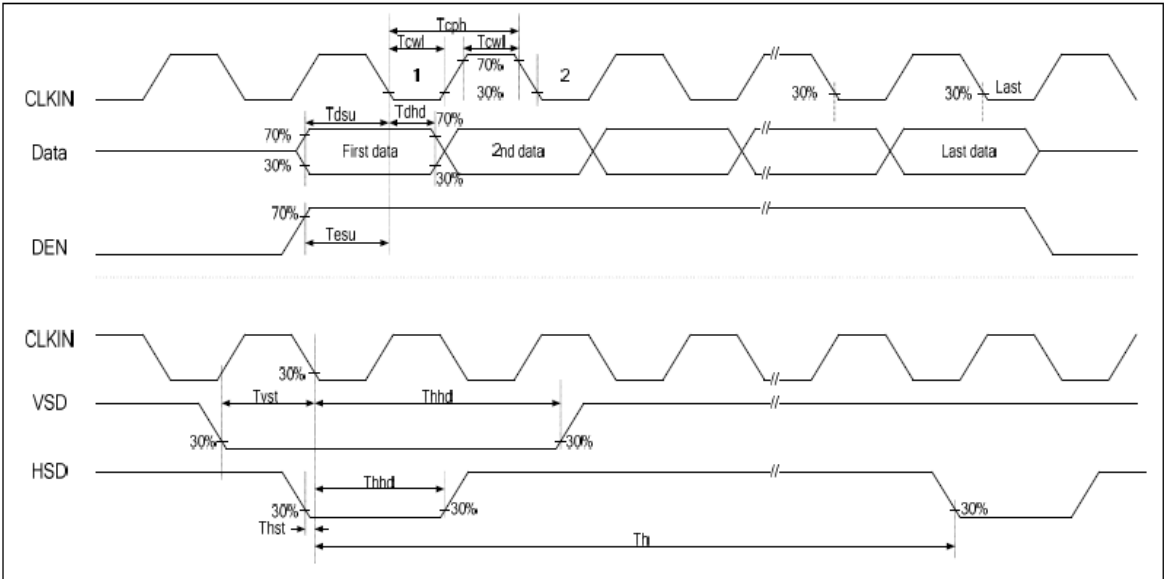
Vertical Timing



6.2 AC Electrical characteristic

Parameter	Symbol	Spec.			Unit
		Min.	Typ.	Max.	
HS setup time	T_{hst}	8	-	-	ns
HS hold time	T_{hhd}	8	-	-	ns
VS setup time	T_{vst}	8	-	-	ns
VS hold time	T_{vhd}	8	-	-	ns
Data setup time	T_{dsu}	8	-	-	ns
Data hold time	T_{dhhd}	8	-	-	ns
DE setup time	T_{esu}	8	-	-	ns
DE hold time	T_{ehd}	8	-	-	ns
VDD Power On Slew Rate	T_{POR}	-	-	20	ms
RSTB pulse width	T_{Rst}	10	-	-	us
CLKIN cycle time	T_{cph}	20	-	-	ns
CLKIN pulse duty	T_{cwh}	40	50	60	%
Output stable time	T_{sst}	-	-	6	ns
Repair OPA Output Stable time	T_{Rsst}	-	-	6	ns

6.3 Input Clock and Data Timing Waveform



7. CTP Specification

7.1 Electrical Characteristics

7.1.1 Absolute Maximum Rating

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	VDD	-0.3	3.47	V	1
I/O Digital Voltage	VDDIO	-0.3	3.47	V	1
Operating temperature	T _{OP}	-20	+70	°C	-
Storage temperature	T _{ST}	-30	+80	°C	-

NOTES:

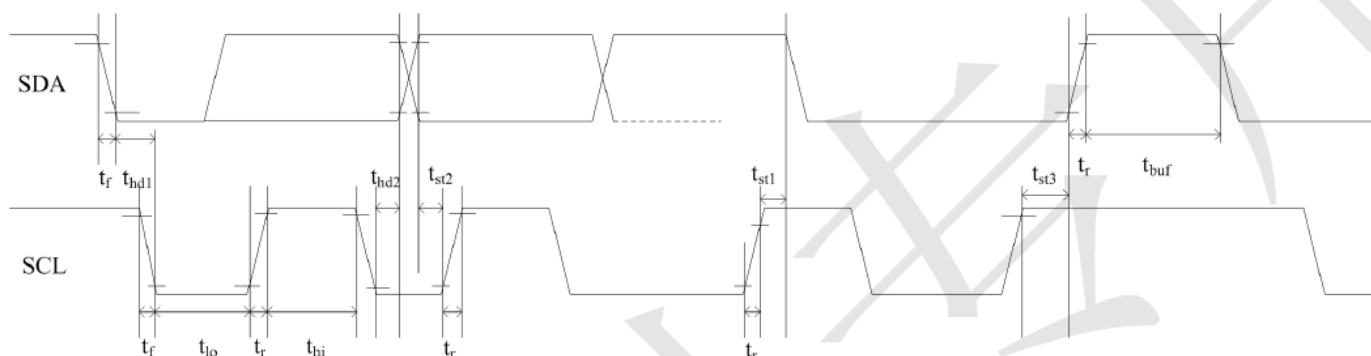
1. If used beyond the absolute maximum ratings, GT9271 may be permanently damaged. It is strongly recommended that the device be used within the electrical characteristics in normal operations. If exposed to the condition not within the electrical characteristics, it may affect the reliability of the device.

7.1.2 DC Electrical Characteristics (Ta=25°C)

Item	Symbol	Min.	Typ.	Max.	Unit	Note
Digital supply voltage	VDD	2.8	-	3.3	V	
I/O Digital supply voltage	VDDIO	1.8	-	3.3	V	
Normal operation mode Current consumption	I _{opr}	-	13		mA	
Green mode Current consumption	I _{mon}	-	4.5	-	mA	
Sleep mode Current consumption	I _{slp}	70	-	120	uA	
Level input voltage	V _{IH}	0.75V _{DDIO}	-	V _{DDIO} +0.3	V	
	V _{IL}	-0.3	-	0.25V _{DDIO}	V	
Level output voltage	V _{OH}	0.85V _{DDIO}	-	-	V	
	V _{OL}	-	-	0.15V _{DDIO}	V	

7.1 Electrical Characteristics

GT9271 provides a standard I2C interface for SCL and SDA to communicate with the host. GT9271 always serves as slave device in the system with all communication being initialized by the host. It is strongly recommended that transmission rate be kept at or below 400Kbps. The I2C timing is shown below:



Test condition 1: 1.8V host interface voltage, 400Kbps transmission rate, 2K pull-up resistor

Parameter	Symbol	Min.	Max.	Unit
SCL low period	t_{lo}	1.3	-	us
SCL high period	t_{hi}	0.6	-	us
SCL setup time for Start condition	t_{st1}	0.6	-	us
SCL setup time for Stop condition	t_{st3}	0.6	-	us
SCL hold time for Start condition	t_{hd1}	0.6	-	us
SDA setup time	t_{st2}	0.1	-	us
SDA hold time	t_{hd2}	0	-	us

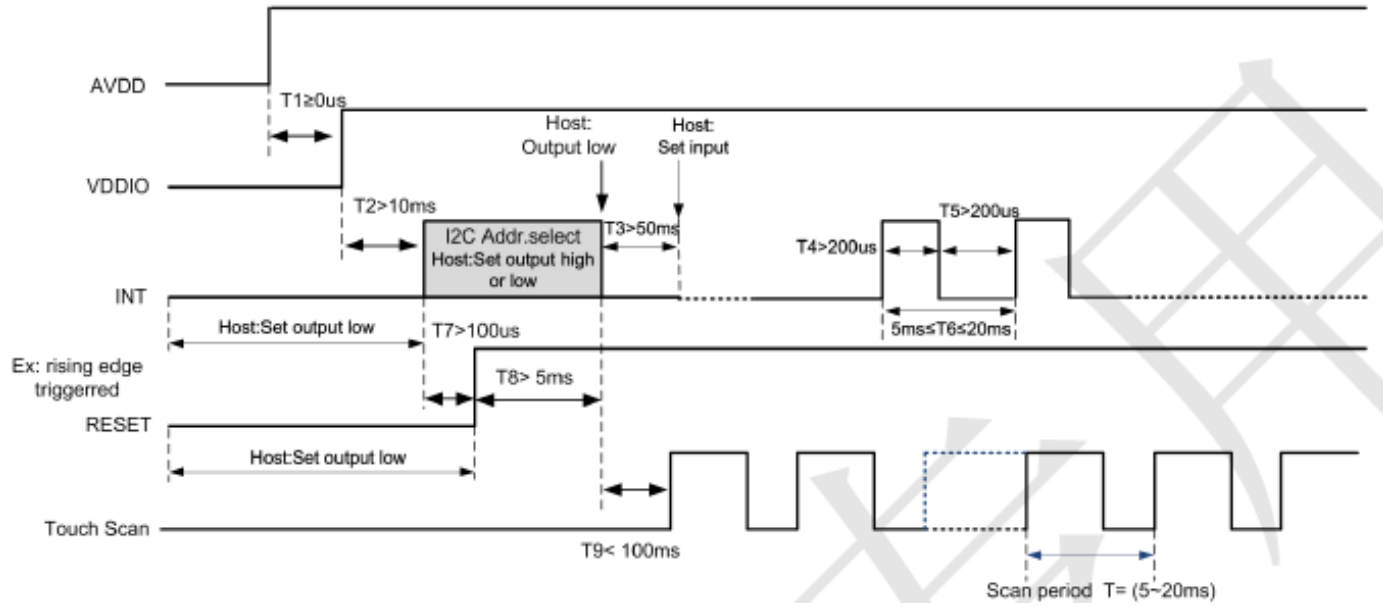
Test condition 2: 3.3V host interface voltage, 400Kbps transmission rate, 2K pull-up resistor

Parameter	Symbol	Min.	Max.	Unit
SCL low period	t_{lo}	1.3	-	us
SCL high period	t_{hi}	0.6	-	us
SCL setup time for Start condition	t_{st1}	0.6	-	us
SCL setup time for Stop condition	t_{st3}	0.6	-	us
SCL hold time for Start condition	t_{hd1}	0.6	-	us
SDA setup time	t_{st2}	0.1	-	us
SDA hold time	t_{hd2}	0	-	us

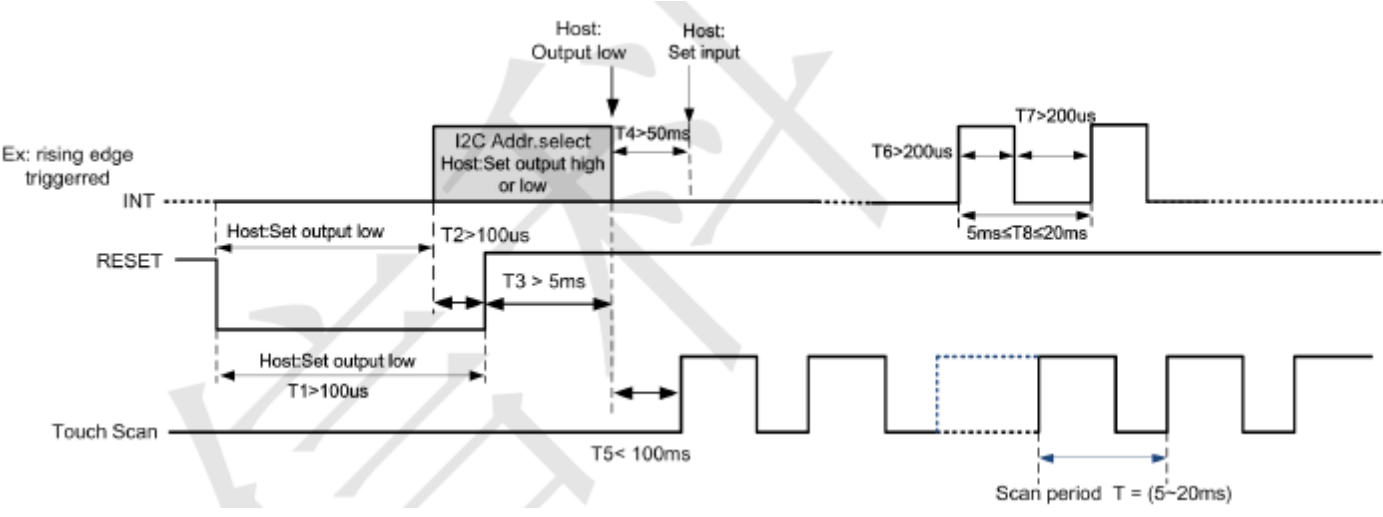
GT9271 supports two I2C slave addresses: 0xBA/0xBB and 0x28/0x29. The host can select the

address by changing the status of Reset and INT pins during the power-on initialization phase. See the diagram below for configuration methods and timings:

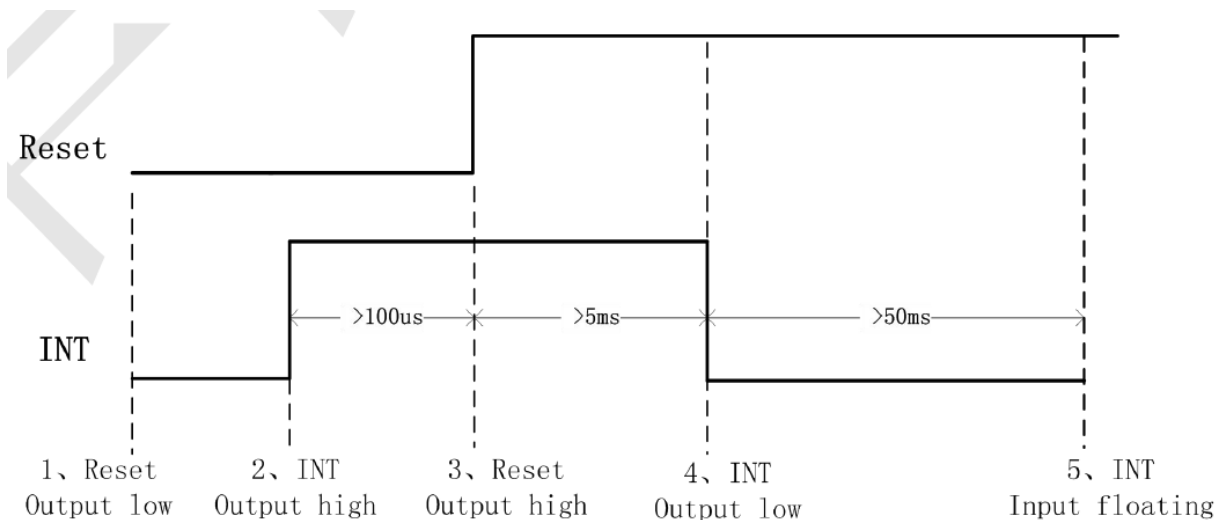
Power-On Timing:



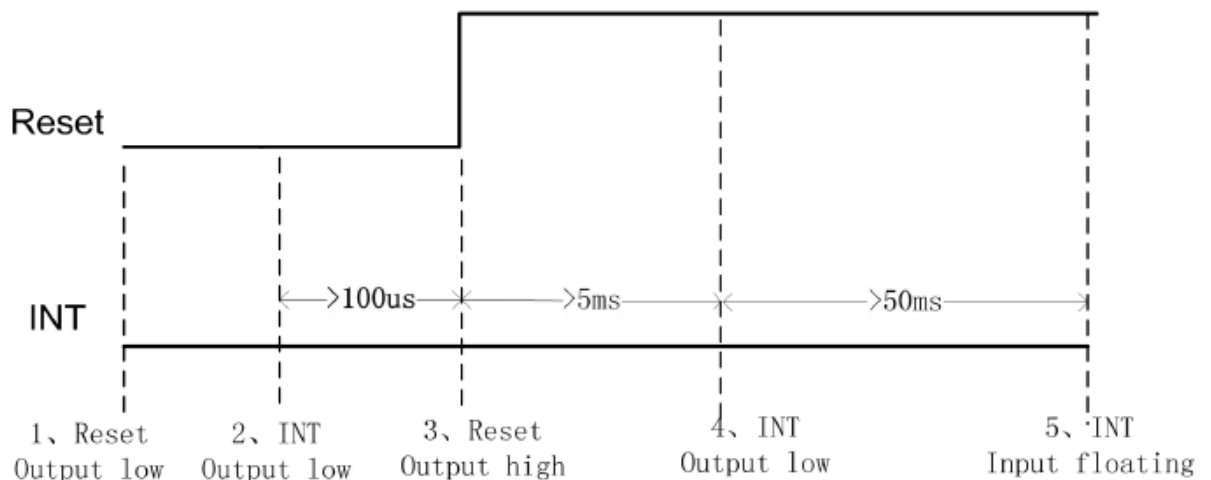
Timing for host resetting GT911:



Timing for setting slave address to 0x28/0x29:



Timing for setting slave address to 0xBA/0xBB:



a) Data Transmission

(For example: slave address is 0xBA/0xBB)

Communication is always initiated by the host. Valid Start condition is signaled by pulling SDA line from high to low when SCL line is high. Data flow or address is transmitted after the Start condition.

All slave devices connected to I²C bus should detect the 8-bit address issued after Start condition and send the correct ACK. After receiving matching address, GT9271 acknowledges by configuring SDA line as output port and pulling SDA line low during the ninth SCL cycle. When receiving unmatched address, namely, not 0xBA or 0xBB, GT9271 will stay in an idle state.

For data bytes on SDA, each of 9 serial bits will be sent on nine SCL cycles. Each data byte consists of 8 valid data bits and one ACK or NACK bit sent by the recipient. The data transmission is valid when SCL line is high.

When communication is completed, the host will issue the Stop condition which implies the transition of SDA line from low to high when SCL line is high.

b) Writing Data to GT9271

(For example: slave address is 0xBA/0xBB)



Timing for Write Operation

The diagram above displays the timing sequence of the host writing data onto GT9271. First, the host issues a Start condition. Then, the host sends 0xBA (address bits and R/W bit; R/W bit as 0 indicates Write operation) to the slave device.

After receiving ACK, the host sends the 16-bit register address (where writing starts) and the 8-bit data bytes (to be written onto the register).

The location of the register address pointer will automatically add 1 after every Write Operation. Therefore, when the host needs to perform Write Operations on a group of registers of continuous addresses, it is able to write continuously. The Write Operation is terminated when the host issues the Stop condition.

c) Reading Data from GT9271

(For example: slave address is 0xBA/0xBB)



Timing for Read Operation

The diagram above is the timing sequence of the host reading data from GT9271. First, the host issues a Start condition and sends 0XBA (address bits and R/W bit; R/W bit as 0 indicates Write operation) to the slave device.

After receiving ACK, the host sends the 16-bit register address (where reading starts) to the slave device. Then the host sets register addresses which need to be read.

Also after receiving ACK, the host issues the Start condition once again and sends 0XBB (Read Operation). After receiving ACK, the host starts to read data.

GT9271 also supports continuous Read Operation and, by default, reads data continuously. Whenever receiving a byte of data, the host sends an ACK signal indicating successful reception. After receiving the last byte of data, the host sends a NACK signal followed by a STOP condition which terminates communication.

8. LCD Module Out-Going Quality Level

8.1 VISUAL & FUNCTION INSPECTION STANDARD

8.1.1 Inspection conditions

Inspection performed under the following conditions is recommended.

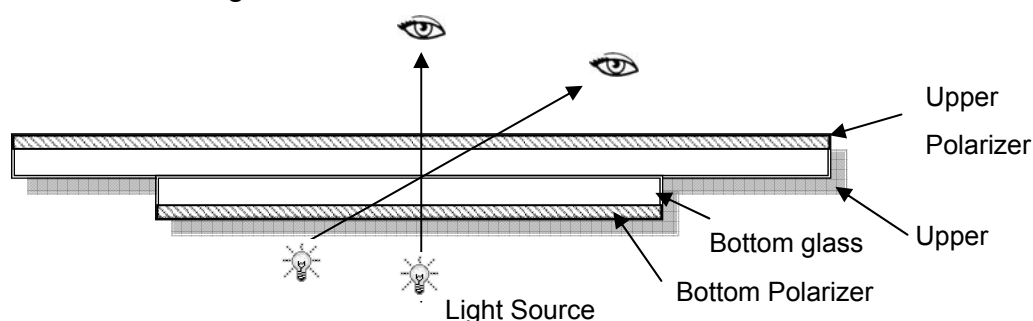
Temperature : $25 \pm 5^{\circ}\text{C}$

Humidity : $65\% \pm 10\% \text{RH}$

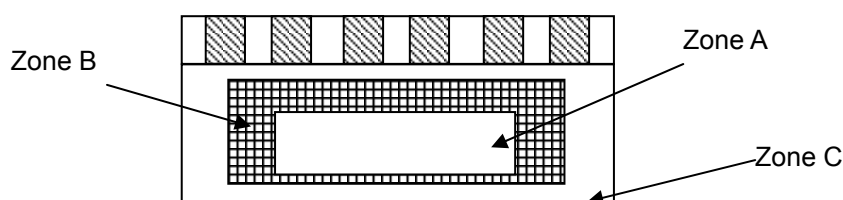
Viewing Angle : Normal viewing Angle.

Illumination: Single fluorescent lamp (300 to 700Lux)

Viewing distance: 30-50cm



8.1.2 Definition



Zone A : Effective Viewing Area(Character or Digit can be seen)

Zone B : Viewing Area except Zone A

Zone C : Outside (Zone A+Zone B) which can not be seen after assembly by customer .)

Note:

As a general rule ,visual defects in Zone C can be ignored when it doesn't effect product function or appearance after assembly by customer.

8.1.3 Sampling Plan

According to GB/T 2828-2003 ; , normal inspection, Class II

AQL:

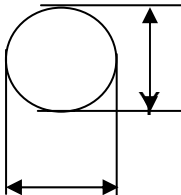
Major defect	Minor defect
0.65	1.5

LCD: Liquid Crystal Display , TP: Touch Panel , LCM: Liquid Crystal Module

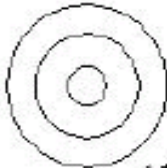
No	Items to be inspected	Criteria	Classification of defects
1	Functional defects	1) No display, Open or miss line 2) Display abnormally, Short 3) Backlight no lighting, abnormal lighting. 4) TP no function	Major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	
4	Color tone	Color unevenness, refer to limited sample	Minor
5	Soldering appearance	Good soldering , Peeling off is not allowed.	
6	LCD/Polarizer/TP	Black/White spot/line, scratch, crack, etc.	

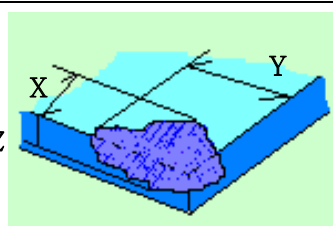
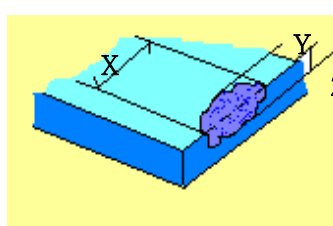
8.1.4 Criteria (Visual)

Number	Items	Criteria(mm)
1.0 LCD Crack/Broken <		

Number	Items	Criteria (mm)																									
2.0	Spot defect  X Φ=(X+Y)/2	① light dot (LCD/TP/Polarizer black/white spot , light dot, pinhole, dent, stain)																									
		Zone Size (mm)	Acceptable Qty			A	B	C	Φ≤0.10	Ignore		Ignore	0.10<Φ≤0.20	3(distance ≥ 10mm)		0.20<Φ≤0.25	2		Φ > 0.25	0							
			Zone Size (mm)	Acceptable Qty																							
		A		B	C																						
		Φ≤0.10	Ignore		Ignore																						
		0.10<Φ≤0.20	3(distance ≥ 10mm)																								
		0.20<Φ≤0.25	2																								
		Φ > 0.25	0																								
		②Dim spot (LCD/TP/Polarizer dim dot, light leakage、 dark spot)																									
		Zone Size (mm)	Acceptable Qty			A	B	C	Φ≤0.1	Ignore		Ignore	0.10<Φ≤0.20	3(distance ≥ 10mm)		0.20<Φ≤0.30	2		Φ > 0.30	0							
			Zone Size (mm)	Acceptable Qty																							
		A		B	C																						
		Φ≤0.1	Ignore		Ignore																						
		0.10<Φ≤0.20	3(distance ≥ 10mm)																								
		0.20<Φ≤0.30	2																								
		Φ > 0.30	0																								
		③ Polarizer accidented spot																									
		Zone Size (mm)	Acceptable Qty			A	B	C	Φ≤0.2	Ignore		Ignore	0.3<Φ≤0.5	2(distance ≥ 10mm)		Φ>0.5	0										
			Zone Size (mm)	Acceptable Qty																							
		A		B	C																						
		Φ≤0.2	Ignore		Ignore																						
0.3<Φ≤0.5	2(distance ≥ 10mm)																										
Φ>0.5	0																										
Line defect (LCD/TP /Polarizer black/white line, scratch, stain)		<table><tr><th rowspan="2">Width(mm)</th><th rowspan="2">Length(mm)</th><th colspan="3">Acceptable Qty</th></tr><tr><th>A</th><th>B</th><th>C</th></tr><tr><td>Φ≤0.03</td><td>Ignore</td><td colspan="2">Ignore</td><td rowspan="3">Ignore</td></tr><tr><td>0.03<W≤0.05</td><td>L≤3.0</td><td colspan="2">N≤2</td></tr><tr><td>0.05<W≤0.08</td><td>L≤2.0</td><td colspan="2">N≤2</td></tr><tr><td>0.08<W</td><td colspan="3">Define as spot defect</td></tr></table>	Width(mm)	Length(mm)	Acceptable Qty			A	B	C	Φ≤0.03	Ignore	Ignore		Ignore	0.03<W≤0.05	L≤3.0	N≤2		0.05<W≤0.08	L≤2.0	N≤2		0.08<W	Define as spot defect		
		Width(mm)			Length(mm)	Acceptable Qty																					
			A	B		C																					
		Φ≤0.03	Ignore	Ignore		Ignore																					
		0.03<W≤0.05	L≤3.0	N≤2																							
		0.05<W≤0.08	L≤2.0	N≤2																							
		0.08<W	Define as spot defect																								

3.0	Polarizer Bubble				
		Zone Size (mm)		Acceptable Qty	
				A	B
		$\Phi \leq 0.2$	Ignore		Ignore
		$0.2 < \Phi \leq 0.4$	3(distance $\geq 10\text{mm}$)		
		$0.4 < \Phi \leq 0.6$	2		
		$0.6 < \Phi$	0		
4.0	SMT	According to IPC-A-610C class II standard . Function defect and missing part are major defect ,the others are minor defect.			

5.0	TP Related	TP bubble/ accidented spot	<table><tr><td rowspan="2">Size Φ(mm)</td><td colspan="3">Acceptable Qty</td></tr><tr><td>A</td><td>B</td><td>C</td></tr><tr><td>Φ≤0.1</td><td colspan="3">Ignore</td><td rowspan="4">Ignore</td></tr><tr><td>0.1<Φ≤0.25</td><td colspan="2">3 (distance ≧</td></tr><tr><td>0.25<Φ≤0.3</td><td colspan="2">2</td></tr><tr><td>0.3<Φ</td><td colspan="2">0</td></tr></table>			Size Φ(mm)	Acceptable Qty			A	B	C	Φ≤0.1	Ignore			Ignore	0.1<Φ≤0.25	3 (distance ≧		0.25<Φ≤0.3	2		0.3<Φ	0	
			Size Φ(mm)	Acceptable Qty																						
				A	B	C																				
			Φ≤0.1	Ignore			Ignore																			
			0.1<Φ≤0.25	3 (distance ≧																						
0.25<Φ≤0.3	2																									
0.3<Φ	0																									
Assembly deflection	beyond the edge of backlight ≤0.15mm																									
Newton Ring	Newton Ring area>1/3 TP area NG																									
	Newton Ring area≤1/3 TP area OK																									

				 似牛顿环						
		TP corner broken X : length Y : width Z : height	<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>X≤3.0mm</td><td>Y≤3.0mm</td><td>Z<LCD thickness</td></tr></table> *Circuitry broken is not allowed.	X	Y	Z	X≤3.0mm	Y≤3.0mm	Z<LCD thickness	
X	Y	Z								
X≤3.0mm	Y≤3.0mm	Z<LCD thickness								
		TP edge broken X : length Y : width Z : height	<table><tr><th>X</th><th>Y</th><th>Z</th></tr><tr><td>X≤6.0mm</td><td>Y≤2.0mm</td><td>Z<LCD thickness</td></tr></table> * Circuitry broken is not allowed.	X	Y	Z	X≤6.0mm	Y≤2.0mm	Z<LCD thickness	
X	Y	Z								
X≤6.0mm	Y≤2.0mm	Z<LCD thickness								

Criteria (functional items)

Number	Items	Criteria (mm)
1	No display	Not allowed
2	Missing segment	Not allowed
3	Short	Not allowed
4	Backlight no lighting	Not allowed
5	TP no function	Not allowed

9. Reliability Test Result

9.1 Condition

Item	Condition	Sample Size	Test Result	Note
Low Temperature Operating Life test	-20℃, 96HR	3ea	pass	-
Thermal Humidity Operating Life test	70℃90%RH, 96HR	3ea	pass	-
Temperature Cycle ON/OFF test	-20℃ ↔ 70℃, ON/OFF, 20CYC	3ea	pass	(1)
High Temperature Storage test	80℃, 96HR	3ea	pass	-
Low Temperature Storage test	– 30℃, 96HR	3ea	pass	-
Thermal Shock Resistance	The sample should be allowed to stand the following 5 cycles of operation: TSTL for 30 minutes -> normal temperature for 5 minutes -> TSTH for 30 minutes -> normal temperature for 5 minutes, as one cycle, then taking it out and drying it at normal temperature, and allowing it stand for 24 hours	3ea	pass	
Box Drop Test	1 Corner 3 Edges 6 faces, 66cm(MEDIUM BOX)	1box	pass	-

Note (1) ON Time over 10 seconds, OFF Time under 10 seconds